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This well-received book, now in fifth edition, has been thoroughly revised and updated with new material on CMOS gates, MSI/ALU and Pentium5 architecture. The chapter on Cache and Virtual Memory has been rewritten. A new chapter on Parallel Computers has been added.

The first part of the book is devoted to digital techniques used in the design of digital circuits and small digital systems. The second part deals with logical organization and architecture of computers. It also describes a small hypothetical computer to illustrate how instruction sets are evolved. Real computers (namely, Pentium and MIPs machines) are described and compared with the hypothetical computer. The remainder of this part describes I/O devices, cache and virtual memory and parallel computers.

The book does not assume extensive knowledge of electronics or mathematics. A knowledge of programming in C or Java would be useful to give the student a proper perspective to appreciate the development of the subject. This textbook is suitable for B.Sc. (Electronics) and B.Tech. courses. Both the parts of the book are self-contained and may be used independently, if appropriate.

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